

FIRESPY

1-BUS SERIES

Gen4

4 2 2 b T  
8 2 2 b T



## PRODUCT OVERVIEW:

The **FireSpy822bT** and **FireSpy422bT** bus analyzers are DapTechnology's latest single-bus IEEE-1394 protocol analyzer. Based on the 4th generation FireSpy analyzer architecture the **FireSpy822bT/422bT** is the most advanced 1394 test equipment in the market. They comprise a significantly more powerful on-board processor and improved connectivity to the host.

The PCIe format takes flexibility to a higher level and addresses the industry's growing support for this data interface. This solution enables users to install the FireSpy hardware in any modern PC that has at least one 1-lane (or higher) PCIe slot available. This allows for installation of the FireSpy in developers' existing PCs or installing the analyzer in a rugged field service laptop or lunchbox computer.

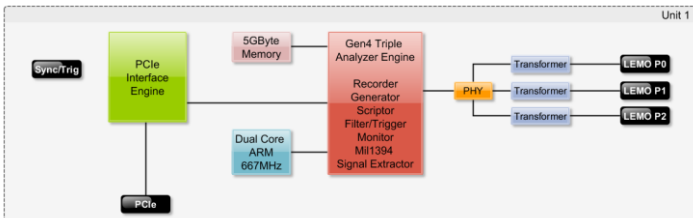
The **FireSpy822bT/422bT** has one 1394 node connected to a single synchronized analysis engine. The engine is controlled by a dual core ARM processor running at 667MHz. Each node is connected to three 1394 ports.

The **FireSpy822bT/422bT** are equipped with 5 GB internal memory and extensive hardware filtering and trigger possibilities. The analyzer can be connected into a host computer using its PCIe interface. On the host you can control the FireSpy using a graphical user interface to analyze and display the bus traffic in a user-friendly way; or you can use the API to program your own control software.

The seamless integration of the SAE AS5643 protocol makes the **FireSpy822bT/422bT** the preferred tool for many Aerospace & Defense development tasks. DapTechnology has taken considerable efforts to fully support the AS5643 protocol in all major functional areas of the **FireSpy822bT/422bT** and continuously updates the analyzer functionality according to implementation requirements and ongoing standardization efforts.

**Key Features** include:

- IEEE 1394b-2008 Beta
  - 100 - 800b transfer rates \*
  - Host connection via PCIe Gen2 1-lane
  - On-board dual-core 667ARM processor and programmable logic
  - 5 GByte internal memory
  - GUI and API for Windows™ Operating Systems
  - Powerful software provides:
    - Monitor
    - Recorder
    - Commander
    - Scriptor
    - Generator
    - Filter and Trigger
    - Support for AS5643, IEC61883, AV/C, SBP2, IP1394 and IIDC protocols
  - Internal SelfTest
  - C/C++ API with wrappers for LabVIEW™
- \* Depending in the exact model



## A COMPLETE SOLUTION:

The **FireDiagnostics Suite** is the most comprehensive collection of 1394 analysis, simulation and interface tools for a wide range of applications. Apart from well established and hardware assisted analyzer tools like Monitor, Recorder, Generator, Commander and Scriptor, the suite also offers a set of software tools designed to integrate the FireSpy products in a wide variety of testing applications, as well as extend customization of its functionality beyond the baseline feature set provided by DapTechnology.

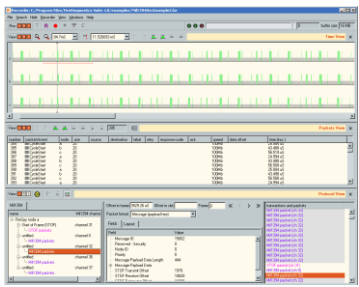
The foundation for all software tools included in the FireDiagnostics Suite is formed by the **Application Programming Interface (API)**. With its interfaces for a wide range of development environments like C/C++ and support for the Windows operating system, the application of FireSpy analyzers is extremely flexible. With its feature-rich function library, all hardware assisted analyzer tools like the Recorder and Generator can be controlled as well as more low-level 1394 bus functions.

The **Recording Viewer** is a standalone application designed to permit trace (recorded data) analysis offline, i.e. without a connected FireSpy. The same comprehensive set of analysis tasks is available but allows for a much smaller PC footprint than having the entire FireSpy application installed.

The **Signal Monitor** is an easy-to-use Mil1394 sub-system monitor and analysis tool that benefits from the hardware-implemented Mil1394 protocol. A customizable set of status signals can be pulled from the bus and displayed in near real-time on a customizable graphical Control Panel. Alarms can be setup to alert the operator of out-of-range values.

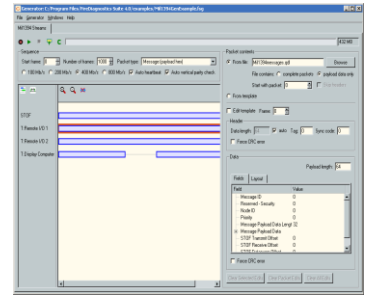
Another cornerstone of the FireSpy products is the unparalleled high-level **protocol support**. Besides the hardware-assisted integration of AS5643 the FireSpys also support software-based analysis capabilities for consumer and industrial control based applications. The different protocols require very different implementation details and are therefore very unique in their implementation. However, some key characteristics can be identified and are listed below:

- Nested protocol header decoding
- Protocol payload separation
- Handshake verification
- Logical grouping of related transactions
- Separate protocol view
- Protocol layer CRC and Parity Check
- Customization of display details



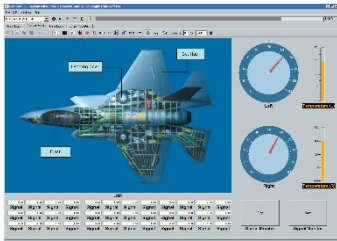
The **Recorder** is the main tool for data traffic capturing and analysis. Running all in HW/FW it guarantees precise time measurement, reliable data capture, instantaneous triggering and enough memory for even very complex analysis tasks. It contains several display views, which can all be switched on or off individually.

- Time View** - timing analysis of events and packets, resolution of 10 ns.
- Packet View** - chronological packet display with Trigger indicator and error verification
- Transaction View** - transaction-oriented display, verification of transaction completeness, transaction list or flow-diagram display
- Topology View** - static bus-topology display at the trace cursor position
- Protocol View** - high-level protocol analysis, encapsulated protocol verification, handshake verification, etc.



The **Generator** is optimized for the generation of isochronous stream data packets offering the most comprehensible feature set for the insertion of errors, streaming of simultaneous channels and payload definitions from stored files.

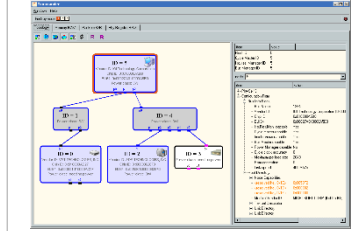
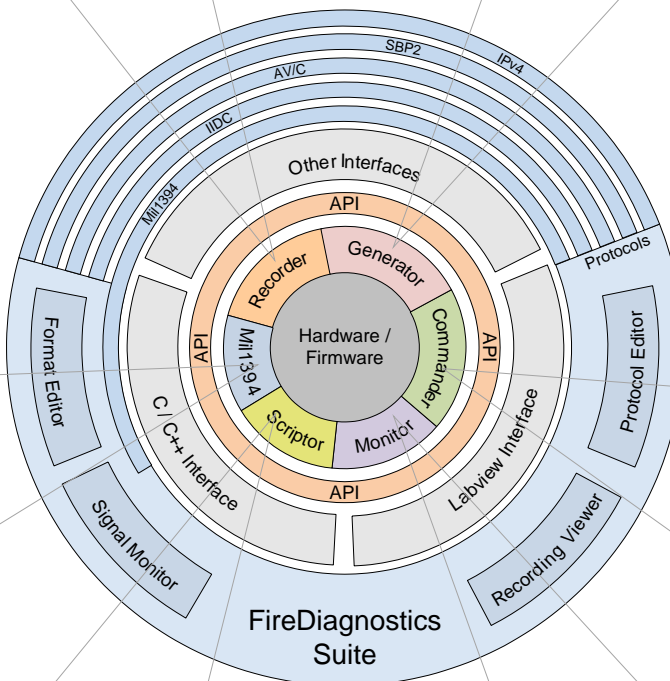
The **Stream Generator** includes a powerful graphical editor to specify slots with stream sequences to be sent for up to 63 channels. Each sequence consists of one or more stream packets with selectable data sources that can be fixed or from file. For each sequence one can select various options such as speed, packet size and header fields, including erroneous values. The overall sequence size is customizable in multiples of Cycle Periods. All Generator slots can be run in a looped-mode continuous transmission. Both the **Stream Generator** and the **Scriptor** can run in parallel for advanced isochronous and asynchronous combination testing.



The **AS5643** protocol HW level support for the FireSpy is an essential component for supporting AS5643. Key features include:

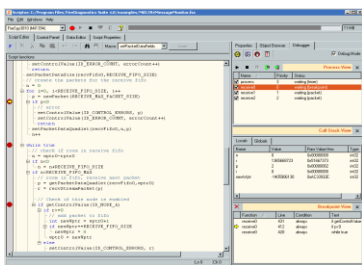
- Generation** - STOF and stream generation, 1µs resolution
- Verification / Calculation** - Timing, Vertical Parity Check, Heartbeat
- Monitoring** - asynchronous stream payload field extraction

**AS5643**



The **Commander** can be used to control the FireSpy functionality on a basic 1394 protocol level:

- Topology** - live display of the current bus topology, Configuration ROM Explorer
- Memory Read/Write** - R/W/L to memory locations of remote nodes
- Packet S/R** - RX/TX of all packets, unformatted and erroneous packets.
- PHY Register** - R/W of PHY registers of the local and R of the remote nodes.

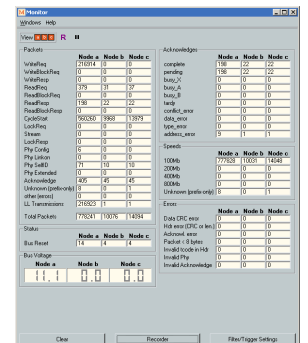


The **Scriptor** permits the definition of C-like scripts to control almost anything on the FireSpy, including sending and receiving packets. It is the preferred tool for the generation of individual asynchronous packets, asynchronous sequences and the simulation of entire handshakes.

- Script editor** - C-like script editor/compiler with automatic code block generation, integrated Debugger and floating-point data type support
- Data editor** - defines data elements that can be used by the script, i.e. generation data
- Control Panel** - display of values using different types of meters (gauge, LCD, thermometer, etc.).

The **Monitor** gives a quick indication of activities on the bus under test. The displayed data is updated in real time.

- Number of packets of specific types
- Number of packets of specific speeds
- Number of acknowledge packets
- Number of error packets
- Total number of packets
- Number of bus resets
- Bus voltage measurement



# MAIN FEATURE SUMMARY:

## GENERAL

- IEEE 1394-2008 Beta compliant
- Supports S100-S800 transfer rates depending on exact model
- Connects to host using PCIe x1 Gen2 interface (single lane)
- 5 GByte memory for embedded OS and packet and data storage
- Firmware field upgradeable to enable future expansions
- AUX connector for:
  - Trigger input and output functions
  - Recording external event
- GUI and API for Windows™ Operating Systems

## MONITOR

- Displays bus activity:
  - isochronous packets
  - all types of asynchronous packets
  - all types of PHY packets
  - all types of acknowledge packets
  - several types of Errors
- Counts packets according to type, speed, ack and error condition
- Counts number of bus resets
- Measurement of bus power voltages (bilingual ports)

## RECORDER

- Time stamping of all packets and status events with 10ns resolution
- Packets hidden by slower connections are visible as 'prefix only' packets
- Extensive packet/event filtering/trigger/search capabilities
  - Packet type
  - Transmission speed
  - Boolean combination of 4 programmable packet sets
  - Data payload patterns
  - Error conditions
  - Various status events
  - Graphical Trigger Sequencer
- Adjustable trigger position within programmable record buffer size
- Cyclic pre-trigger buffer management option
- Different kinds of packet display views, including:
  - Time View, displays all packets on a time line, including the prefix
  - Packet View, displays packets as list plus selected packet options
  - Transaction View, displays transactions as list or flow graph
  - Topology View, graphical topology displays as is during recording
  - Protocol View, displays packets decoded to selected protocol
- Precise time measurements
- Marking of individual packets or packet ranges
- Export format for re-generation of packets by Scriptor or API

## GENERATOR

- Simultaneous generation of up to 63 iso streams
  - Graphically programming of stream transmit block
  - Data payload import from file
- Generator and Scriptor run simultaneous for stream and asynchronous packet generation
- Special Mil1394 stream generator package (optional)

## SCRIPTOR

- Script Editor
  - C-like scripting language
  - Function Library
  - Macros to automatically generate blocks of code
  - Syntax coloring
  - Integrated Debugger
  - Floating point data types
- Data Editor
- Control Panel
  - Graphical display elements for data value representation
  - Ethernet-connected Client Panels for remote data monitoring
- Several Sample Scripts

## COMMANDER

- Reading and/or writing of local and reading of remote PHY registers
- Reading and/or writing of remote memory locations (incl. CSR register space)
- Possibility to graphically view the current Topologies
- Sending of user definable packets

# SPECIFICATION:

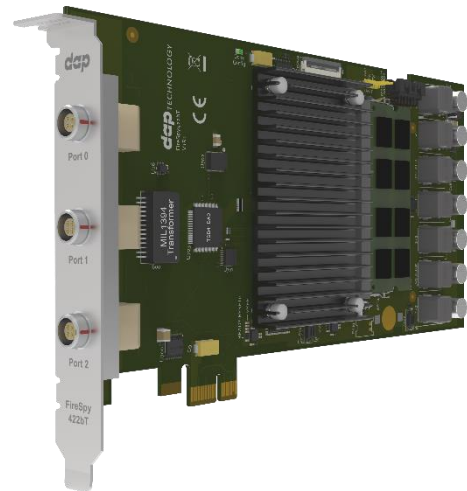
<b>Dimensions:</b>	PCIe x1, card length approx. 205 mm
<b>Operating Range:</b>	0 – 70 C
<b>Power Requirements:</b>	max. 25 W
<b>Compliance:</b>	FCC Class A
<b>Connections:</b>	PCIe x1 Gen2 3x LEMO connectors
<b>Indicators:</b>	-
<b>Switches:</b>	-
<b>Package Content:</b>	FireSpyx22bT 3x LEMO to Bilingual cables
<b>Product warranty:</b>	36 months limited warranty
<b>Part Number:</b>	FS822bT or FS822bTAS5643 (analyzer with AS5643 SW protocol package)  FS422bT or FS422bTAS5643 (analyzer with AS5643 SW protocol package)

FSx22bT

bT:	IEEE-1394 Beta, S200-S800 Transformers
bT1:	IEEE-1394 Beta, S100-S200 Transformers (TSB41-only)
	IEEE-1394 LEMO Connectors
	PCIe Host Connectivity
4:	S100-S400 IEEE-1394 Ports (TSB41)
8:	S100-S800 IEEE-1394 Ports (TSB81)

## SW Add-on modules:

SBP2 protocol software package  
IIDC protocol software package  
AV/C protocol software package  
IP1394 protocol software package  
AMI-C protocol software package  
AS5643 protocol software package



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